OIPE JC17
NOV 22 2004
PATENT & TRADEMARK OFFICE

IFW

PATENT
Customer No. 22,852
Attorney Docket No. 02887.0256-00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	)
Masaru Kito	) Group Art Unit: 2818
Serial No.: 10/685,422	) Examiner: Tran, Mai Huong C
Filed: October 16, 2003	) Confirmation No.: 5841
For: SEMICONDUCTOR MEMORY AND METHOD OF MANUFACTURING THE SAME	)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

RESPONSE TO RESTRICTION REQUIREMENT

In a restriction requirement dated October 26, 2004, the Examiner required restriction under 35 U.S.C. § 121 between Group I, claims 1-7 and Group II, claims 8-17. Applicants provisionally elect to prosecute Group I, claims 1-7, characterized by the Examiner as drawn to a semiconductor device, without traverse.

Please grant any extensions of time required to enter this response and charge any additional required fees to our deposit account 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER, L.L.P.

Dated: November 22, 2004

By: _____
Richard V. Burgujian
Reg. No. 31,744